

Francisco Membibre received the M.Sc. degree in Telecommunications Engineering from the Universidad de Alcalá (UAH), specialized in advanced electronics system in the space field. He was researcher in national projects with the Electronics Department, UAH, implementing medium access techniques, synchronism methods and drivers in SoCs (System-on-Chip). Actually, he is HW-SW designer at DEIMOS Space in Embedded System Group, participating in projects such as EO-ALERT, SAT4EO and others implementing all its algorithms on a Zynq UltraScale+ MPSoC using all current techniques (p.e. FPGA programming, high-level programming, embedded linux, autocoding, etc.).