In-Plane Germanium Nanowire Networks: Growth and Hole Transport

S. Ramanandan¹, A. Morelle², S. Ben-David¹, S. Martí-Sánchez³, A. Rudra⁴, J. Arbiol^{3,5}, T. Ihn ^{2,6}, K. Ensslin ^{2,6} and A. Fontcuberta i Morral^{1,4,7}

¹Laboratory of semiconductor Materials, Institute of Materials, EPFL, 1015 Lausanne, Switzerland ²Solid State Laboratory, ETH Zurich, 8093 Zurich, Switzerland

³Catalan Institute of Nanoscience and Nanotechnology (ICN2), CSIC and BIST, Campus UAB, Bellaterra,

Barcelona, Catalonia, Spain

⁴Institute of Physics, Faculty of Basic Sciences, EPFL, 1015 Lausanne, Switzerland

⁵ ICREA, Pg. Lluís Companys 23, 08010 Barcelona, Catalonia, Spain

⁶ Quantum Center, ETH Zurich, 8093 Zurich, Switzerland

⁷ Center for Quantum Science and Engineering, EPFL, 1015 Lausanne, Switzerland

santhanu.ramanandan@epfl.ch

Holes in Ge-Si core-shell nanowires are promising candidates for realizing spin-based quantum computers. The strong and tunable spin-orbit interaction of holes enables fast qubit manipulation, and the low susceptibility to hyperfine noise improves coherence [1]. In addition, the shape and orientation of the nanowires can be tailored to eliminate the influence of charge and hyperfine noise on qubit coherence.

The conventional method for producing Ge-Si nanowires employs the Au-catalyzed vapor-liquid-solid (VLS) process, which restricts the growth of nanowires to an out-of-plane configuration [2]. In contrast, selective area epitaxy (SAE) is a reliable method to obtain in-plane nanowires at the site of future devices in a scalable manner [3][4].

This work presents the first report of SAE of in-plane Ge nanowires inside raised Si membranes with a V-groove. Figure 1, in the inset, shows the cross-section of the Ge nanowire. The Ge nanowires, grown inside a Si V-groove, can be entirely encapsulated by the Si shell, thus avoiding direct contact between the Ge channel and the silicon dioxide (SiO_2) dielectric (see Figure 1 inset). Additionally, the triangular shape of the nanowire tip helps to induce anisotropic strain in the Ge channel. Finally, having Ge nanowires on elevated structures facilitates the development of conformal gating schemes, allowing better control and tunability of charge carriers.



Fig. 1. I versus V_G plot of nanowire field-effect measurement at 1.3 K. The inset figure shows the cross-section of the Ge nanowire on an elevated Si V-groove.

We investigated the underlying growth mechanism and the electrical properties of the Ge nanowires. Using scanning electron microscopy (SEM) and atomic force microscopy (AFM), we report the temporal evolution of Ge nanowire growth. The growth of the nanowires within the V-groove occurs through the nucleation and coalescence of the Ge islands. In addition, the nanowires were analyzed under high-angle annular dark-field scanning transmission electron microscopy (HAADF-STEM) to assess their crystal quality and faceting. The fully grown Ge nanowires exhibit a zinc-blende structure with twins at the Ge nanowire Si(111) interface.

The electrical properties of the nanowires were evaluated using field effect and Hall measurements on Ge nanowire networks. The nanowires exhibit p-type conductivity. Magnetotransport measurements at low temperatures reveal quantum diffusive transport phenomena such as universal conductance fluctuations and weak antilocalization. Finally, we demonstrate the fabrication of electrostatically defined quantum dots in SAE Ge nanowires.

References

- [1] G. Scappucci et al. Nature Reviews Materials 6, 926–943 (2021).
- [2] S. Conesa-Boj et al. Nano Letters 17, 2259–2264 (2017).
- [3] S. Ramanandan et al. Nano Letters 22, 4269-4275 (2022).
- [4] S. Ramanandan et al. Nanoscale Horizons 9, 555 (2024).